



GIET UNIVERSITY, GUNUPUR – 765022 M.C.A (Third Semester) Examinations, January – 2023 MCA20301 - Compiler Design

Maximum: 70 Marks Time: 3 hrs

| The figures in the right hand side indicate marks PART – A | | $(2 \times 10 = 20 \text{ Marks})$ | | |
|------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|---------|-------|
| Q1. | Answer ALL questions | | CO# | PO# |
| a. | Write the differences between compiler and interpreter. | | 2 | 1 |
| b. | Mention the steps involved in Code Optimization Technique | | 2 | 2 |
| c. | What is cross compiler? | | 1 | 1 |
| d. | Define LR(1) item. | | 2 | 2 |
| e. | Find the First and Follow of the following grammar. | | | |
| | $S \rightarrow aABb$ | | 2 | 2 |
| | $A \rightarrow c \mid \varepsilon$ | | 3 | 2 |
| | $B \rightarrow d \mid \varepsilon$ | | | |
| f. | Define Code Optimization. | | 3 | 1 |
| g. | Define Boot Strapping. | | 1 | 2 |
| h. | Mention all Top Down and Bottom Up Parsers. | | 1 | 1 |
| i. | Mention the issues of Code Generation. | | 2 | 1 |
| j. | Mention the Loop Optimization Techniques. | | 2 | 1 |
| PART – B | | $(10 \times 5 = 50 \text{ Marks})$ | | |
| Ansv | ver ANY FIVE questions | Mark | cs CO ‡ | # PO# |
| 2. | Describe the phases of compiler design in detail and write the output for each phase of the compiler design for the following instructions. | ne 10 | 2 | 1 |
| | a. M=N+O+P*100b. X=Y*Z*90 | | | |
| 3a | Check whether the following grammar is in LL(1) or not and Construct the predictive parsing table for it. | 6 | 2 | 2 |
| b | $S \rightarrow AB \mid eDa$ $A \rightarrow ab \mid c$ $B \rightarrow dC$ $C \rightarrow eC \mid \varepsilon$ $D \rightarrow fD \mid \varepsilon$ Write a note on Synthesized attributes and Inherited attributes with examples. | 4 | 2 | 2 |
| 4a | Construct SLR Parsing Table for the following grammar | 6 | 2 | 2 |
| | S→L=R S→ R L→*R L→id R→L | | | |
| 4b | What is Left Recursion? Remove Left Recursion from the following grammar. $S \rightarrow SS+ SS^* a$ | 4 | 2 | 2 |
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5 a. Write a note on Error Detection and Recovery.
                                                                                                    2
                                                                                                           1
                                                                                                    2
 b. Define DAG. Represent the DAG for following Three Address Code.
                                                                                             5
                                                                                                           1
     T1=a+b
     T2=a-b
     T3=T1*T2
     T4=T1-T3
     T5=T4+T3
     Describe Peephole Optimization in detail.
                                                                                             5
                                                                                                    2
                                                                                                           2
     Write a note on Symbol Table Management
                                                                                             5
                                                                                                    2
                                                                                                           2
    Define TAC. Write the Quadruple & triple for the following expression
                                                                                             5
                                                                                                    3
                                                                                                           3
7a.
     X = -Y * (Z+M)
     Define SDD. Write the SDD for S \rightarrow if(A) B1 else B2
                                                                                             5
                                                                                                    4
                                                                                                           2
     Mention the basic blocks for the instruction
                                                                                             5
                                                                                                    4
                                                                                                           1
     p=1;
     for (i=2; i<=x; i++)
          p=p*i;
     p=p+1;
 b. Write a note on Register Allocation and Register Assignment
                                                                                             5
                                                                                                           1
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--- End of Paper ---