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QP Code: RM22MCA003	Reg.						AR 22



GIET UNIVERSITY, GUNUPUR – 765022

M.C.A (First Semester) Examinations, March - 2023

MCA20102— Computer Organization and Architecture

Time: 3 hrs Maximum: 70 Marks (The figures in the right hand margin indicate marks.) PART - A $(2 \times 10 = 20 \text{ Marks})$ CO# Blooms Q.1. Answer all questions Level CO1 K4 Distinguish between Computer Organization and Computer Architecture. CO₁ **K**1 Write the structure of buses used in Computer System. b. CO1 K1 c. Define a micro instruction with an example. CO2 K1 What is the use of Complements? Discuss 1's and 2's Complement? d. CO2 What is the use of Normalization in Floating Point Numbers? **K**1 e. CO3 K1 What is the use of ROM and types of ROM? f. CO3 What is the use of Pipelining concept with a neat diagram? **K**1 g. CO3 K1 h. Define Cache Memory. CO4 K1 i. What is the difference between Synchronous and Asynchronous Data transfer? CO4 **K**1 Define Interrupt and types of Interrupts. PART - B $(10 \times 5 = 50 \text{ Marks})$ Marks CO# Answer ANY FIVE questions Blooms Level K1 CO₁ 5 Write about Reduced Instruction Set Computer with the help of a block diagram. 5 CO₁ **K**1 b. Draw the flowchart of an Instruction Cycle and explain. CO₁ K1 5 What is the purpose of addressing modes? Explain various addressing techniques. CO₁ K4 b. Distinguish between Microprogrammed Control and Hardwired Control. 5 5 CO2 K1 Explain in detail about Fixed Point Representation. 5 CO2 K2 Show (85.88)₁₀ in single and double Precision formats. K2. Discuss Booth's Multiplication Algorithm with a neat flowchart. 5 CO2 5.a. 5 CO2 K2 b. Divide 101011 with 101101 and show the result in binary and verify the result. 5 CO3 K1 What is the use of Arithmetic Pipeline with a neat flowchart. 6. a. b. Discuss in detail about SIMD concept. 5 CO3 K2 CO3 K1 5 7.a. Explain Cache Memory with Associative Mapping Technique. CO4 5 **K**1 b. Explain ROM with a neat block diagram and its function table. 5 CO4 K4 8. a. Distinguish between Programmed I/O and Memory I/O. CO4 5 **K**1

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What is the use of DMA? Discuss with a block diagram.