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Total number of printed pages - 3

B. Tech

PCCS 4305

RAL LIBA

Sixth Semester Regular Examination - 2015

COMPILER DESIGN

BRANCH: IT

QUESTION CODE: J 442

Full Marks - 70

Time: 3 Hours

Answer Question No. 1 which is compulsory and any five from the rest.

The figures in the right-hand margin indicate marks.

Answer the following questions :

2×10

- (a) What is the difference between compiler and interpreter? Give at least four differences.
- (b) What is a rational preprocessor? Explain its functionality with an example.
- (c) Given expression Value = x + y * 10, what will be the output of the lexical analysis phase?
- (d) What are the drawbacks of SR parser?
- (e) What is type checking? Give an example of type checking in SDD.
- (f) What is the difference between s-attributed definition and I-attributed definition?

$$A := -B * (C * D)$$

- (g) How syntax directed translation is different from SDD? Explain.
- (h) What is the difference between register-descriptor and address-descriptor? Explain.
- (i) What is activation? Explain with an example.
- (j) What is dead code elimination? Explain.
- Write down the algorithm for conversion of a regular expression into ∈ -NFA and subsequently NFA to DFA. Use the algorithms to convert the regular expression RE = (a|b)*abb into ∈-NFA and then to DFA.

 (a) What is predictive parsing approach? Draw the predictive parsing table for the following grammar.

(b) What is shift reduce conflict? Draw the SLR parsing table for the following grammar and show whether any conflict is present or not.

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 What is LALR? Why it is used? Write the algorithm to construct LALR parsing table. Draw a LALR parsing table using an example.

5. (a) What is SDD? Why it is required? Write down the SDD for the following set of production rules:

$$E \rightarrow (E1)$$

$$E \rightarrow I$$

$$I \rightarrow I 1 digit$$

(b) What is three address code? What are the different forms of three address code? Explain quadruple, triple, indirect triple representation of three address code

$$(a + b)*c/d.$$

 (a) What is symbol table? Describe the various approaches of symbol table organization.

(b) How to handle activation records for calling sequences? Explain.

5

7. (a) How do you define basic blocks? Find out the basic blocks for a source code as given below:
5

```
w=0;
x=x+y;
y=0;
if (x>z)
   { y=x;
   x++;
}
else
   { y=z;
   z++;
}
w=x+z
```

Also draw the flow graph for it.

(b) Write down the algorithm to construct a DAG. Construct the DAG for the following basic block:

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Write short notes on any two:

- (a) Error reporting and recovery
- (b) Handle purning
- (c) Dependency graph
- (d) Peephole optimization.

5×2