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**Total Number of Pages: 01** 

B.TECH PCCS4305

## 6<sup>th</sup> Semester Regular Back Examination 2016-17 COMPILER DESIGN

BRANCH(S): IT, ITE Time: 3 Hours Max Marks: 70 Q.Code- Z680

Answer Question No.1 which is compulsory and any five from the rest.

The figures in the right hand margin indicate marks.

Q1	a) b) c) d) e) f) g) h) i)	Answer the following questions:  Differentiate between compilers and cross compiler?  List the role of error handler.  What do you mean by Passes?  Define syntax analysis with an example.  Write the characteristics of syntax analysis.  What is optimization? List different methods for code optimization.  What do you mean by ambiguity? Explain with an example.  What do you mean by semantics of a programming language?  Differentiate between NFA and DFA?  What do you mean by leader in the context of flow graph?	(2 x 10)
Q2	a) b)	Discuss the role of lexical analyzer in detail.  Construct the minimized DFA for the regular expression (0+1)*(0+1) 10.	(5) (5)
Q3		Describe the various phases of complier and trace the program segment $4: * + = c b$ a for all phases.	(10)
Q4	-	Explain the error recovery strategies in syntax analysis.  Define three-address code. Describe the various methods of implementing three address statements with an example	(5) (5)
Q5	-	Discuss the various storage allocation strategies in detail.  Discuss the various methods for translating Boolean expression.	(5) (5)
Q6	•	How to generate a code for a basic block from its dag representation? Explain.  Discuss the advantage and disadvantage of LR parsing Scheme.	(5) (5)
Q7		Write an algorithm to construct the natural loop of a back edge. Discuss briefly about Peephole Optimization.	(5) (5)
Q8	a) b) c) d)	Write Short Notes (Any Two) Synthesized attributes. Lex Compiler. Handle pruning. DAG	(5 x 2)