210	210	210		210		210	210	2
Registra	ation No:							
Total Number of Pages: 02								B.Tech.
PEL								PEL5I101
5 th Semester Regular Examination 2017-18 Power Electronics								
040	040		Power El		i	040	040	
210	210	210	Time: 3			210	210	2
Max Marks: 100								
Q.CODE: B402								
Answer Question No.1 and 2 which are compulsory and any four from the rest. The figures in the right hand margin indicate marks.								
The figures in the right hand margin indicate marks.								
Q1	Answer the following questions: multiple type or dash fill up type							(2x10)
²¹⁰ a)	The maximum di		•	olto do		210	210	2
	 (i) directly proportional to Vm of supply voltage (ii) inversely proportional to Vm of supply voltage (iii) directly proportional to L in the circuit (iv) inversely proportional to L in the circuit 							
L)								
D)	b) Two identical SCRs are connected back to back in series with a load. If each SCR is fired at 900, a PMMC voltmeter across the load would read							
	(i) peak voltage							
210	(ii) (1/2) x peak \			210		210	210	2
	(iii) (1/π) × peak voltage (iv) zero							
c)	A forward voltag	e can be appl	ied to an S	CR after it	S			
(i) anode current becomes zero								
	(ii) anode voltage			come zero	at the s	ame time	<u>م</u>	
	(iii) anode voltage and anode current become zero at the same time(iv) gate current becomes zero							
²¹⁰ d)	In a single-phase full converter, for continuous conduction, each pair of SCRs							
e)	conduct for In a three phase, half-wave diode rectifier, the ratio of average output voltage							
c,	to per phase maximum a.c. voltage is,							
	(i) 0.955 (ii) 0.827							
f)	(iii) 1.654 Why is a resistor	iv) 1.169) connected a		ate and ca	ithode c	of an SCF	37	
•,	(i) To protect aga		_	jato aria oa	ili lodo c	71 all 001	` .	
210	(ii) To bypass the			210		210	210	2
	(iii) To protect ag	•	•	rise				
g)	A chopper can b	e used on	·					
	(i) pulse-width m	•		quency mo		n only		
h)	(iii)amplitude mo A single-phase	•	, ,	ith PWM ar designed b		a thvrist	ors without	
,	A single-phase bridge inverter can be designed by having thyristors without forced commutation circuitry if the load it is handling is							
210	(i) series combin						210	2
	(ii) series combir (iii) series com						tance with	
(iii) series combination of resistance, inductance and capacitance with resonant frequency of the circuit being lower than the inverter switching								
	frequency (iii) series combination of resistance, inductance and capacitance with							
	resonant freque							
	frequency	•					3	
210 i)	SMPSs are super (i) size and efficition		oower supp	olies in res	pect of,	210	210	2
	(ii) efficiency and	•						
	(iii) regulation an	nd noise						
(iii) noise and cost								