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	Registration No:					
Tot	al Number of Pages :	${_{\scriptstyle\perp}}{_{\scriptstyle\perp}}{_{\scriptstyle\perp}}$			в.тесн	
100	arramber of ruges.		ester Regular Examinat	ion-April-May 2019	<b>D.ILCII</b>	
			4030 Microprocessor	ž		
		(Re	egulations 2017) AEIE			
	Time: 3 Hours		A ATT 0		n : 100 Marks	
		The figu	Answer ALL Queres in the right hand ma			
				estions) 10 x 2=20 Mark		
	Q.1. Answer <u>All</u>		(ividicipie choice Que	(Strong) 10 21 2—20 1/141 11		
a		_	ty of 8085 microproc	ressor?	[CO1][PO1]	
а	a) 24KB	b) 44KB	c) 64KB	d) 84KB	[COI][IOI]	
b	Which register pair a	,	,	u) o neb	[CO1][PO1]	
Ü		b) PC	c) DL	d) HL	[001][101]	
c	,	,	,	ng and loop instructions.	[CO1][PO2]	
	a) DX	b) CX	c) BX	d) AX		
d	The internal RAM n	nemory of the 8	051 is	,	[CO2][PO1]	
	a) 32 bytes	b) 64 bytes	c) 128 bytes	d) 256 bytes		
e	All the functions of	the ports of 825	5 are achieved by pro	ogramming the bits of an	internal [CO2][PO2]	
	register called					
		•	ontrol c) control wo			
f		provides a maxi	mum of seven	segment display interface	ce with [CO2][PO2]	
	CPU.	1) 1.5	\ <u>.</u>	1) 40		
~	a) 8	b) 16	c) 32	d) 18	[CO2][DO1]	
g	Which of the follow	_	<del>-</del>	1) 77 (1	[CO3][PO1]	
1.			c) Overflow fla		[CO2][DO2]	
h		-	of size	•	[CO3][PO2]	
	,	b) 6	c) 4	d) 12	[CO 4][DO1]	
i	The 8051 can handle	b) 5	•	J) 7	[CO4][PO1]	
;	a) 4		c) 6	d) 7	[CO4][DO2]	
J	Identify the non-mas a) RST 7.5	b) RST 6.5	c) RST 5.5	d) RST 4.5	[CO4][PO2]	
	a) KS1 7.3	0) K31 0.5	C) KS1 3.3	u) K31 4.3		
		PART _ R.	Short Answer Oues	tions) 10x2=20 Marks		
	Q.2. Answer <u>AL</u>		Short Miswer Ques	tions) 10A2—20 Willing		
	Qv=v 11115 (; v1 1 <u>1121</u>	4.000.010				
a	Explain with an exam	mple, how a 20	bit physical address	is generated in 8086?	[CO1][PO1]	
b						
c	<u> </u>					
d	List the operation modes of 8255.					
e	6 6					
f						
g	Define pipelining. [CO3][PO3]					
h :	Explain the 16-bit registers DPTR and SP of 8051. [CO4][PO1]					
i	What are 8086 interrupt types? [CO4][PO2] What are the functions performed by 8251? [CO4][PO2]					
j	vv nat are the function	ns periornieu b	y 0231;		[CO4][PO2]	



## **PART – C: (Long Answer Questions) 4x15=60 Marks**

## Answer $\underline{ALL}$ questions

Q.3	3							
a	Briefly discuss the instruction sets of 8085 with examples.	7	[CO1] [PO1]					
b	Let at the program memory location 4080, the instruction MOV B, A (opcode	8	[CO1] [PO2]					
	47H) is stored while the accumulator content is FFH. Illustrate the execution							
	of this instruction by timing diagram.							
	OR							
c	Briefly explain the interrupts for 8085 with neat circuit diagram and also	10	[CO1] [PO1]					
	discuss the SIM and RIM instruction format.	10						
d	Write an assembly language program to calculate the sum of series of data	5	[CO1] [PO2]					
	using 8085 microprocessor.							
Q.4								
a	With neat diagram describe the working of 8086 in minimum mode bus cycle.	8	[CO2] [PO1]					
b	Explain the following instruction with examples	7	[CO2] [PO2]					
	i) CMP ii) XLAT iii) XCHG iv) DIV v) DAA vi) LDS vii) SHR							
	OR							
c	Explain the various string manipulation instructions with example.	8	[CO2] [PO1]					
d	Interface two 4K X 8 EPROMS and two 4K X 8 RAM chips with 8086,	7	[CO2] [PO2]					
	microprocessor and draw the suitable circuit showing their interfacing?							
Q.5	Q.5							
a	Explain the working of different blocks of 8254 PIT with a neat figure.	7	[CO3] [PO1]					
b	With block diagram, explain working principle of 8255 PPI.	8	[CO3] [PO2]					
	OR							
c	Draw and discuss internal block diagram of 8251 USART.	8	[CO3] [PO2]					
d	Draw and explain block diagram of 8259 PIC.	7	[CO3] [PO3]					
Q.6	$\delta$							
a	Explain briefly the addressing modes of 8051 with example.	7	[CO4] [PO1]					
b	Explain briefly the interrupts of 8051, indicate their vector address.	8	[CO4] [PO2]					
	OR							
c	List all the registers used in 8051 microcontroller in brief.	6	[CO4] [PO1]					
d	Write an assembly language program for 8051 to generate square ware of 1	9	[CO4] [PO2]					
	KHZ on port pin P1.1. User timer 1 and assume crystal frequency to be 12							
	MHZ. Clearly show the necessary calculation.							